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Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 20 Issue: 11 , Nov. 2001

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Solid-State Circuits, IEEE Journal of , Volume: 36 Issue: 1 , Jan. 2001

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Communications, 2001. ICC 2001. IEEE International Conference on , Volume: 4 , 2001

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**4 256-pixel CMOS-integrated thermoelectric infrared sensor array**

*Schaufelbuchl, A.; Munich, U.; Menolfi, C.; Brand, O.; Paul, O.; Huang, Q.; Baltes, H.*

Micro Electro Mechanical Systems, 2001. MEMS 2001. The 14th IEEE International Conference on , 2001

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**5 A scalable SDH/SONET framer architecture for datacom and telco applications**

*Herkersdorf, A.; Buchmann, P.; Clauberg, R.; Lemppenau, W.; Schindler, H.R.; Webb, D.*

Broadband Communications, 2000. Proceedings. 2000 International Zurich Seminar on , 2000

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**6 A 3 GHz, 32 dB CMOS limiting amplifier for SONET OC-48 receivers**

*Sackinger, E.; Fischer, W.C.*

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**7 A 0.6 W 10 Gb/s SONET/SDH bit-error-monitoring LSI**

*Kawai, K.; Ichino, H.*

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**8 0.6  $\mu\text{m}$  CMOS, 622/155 Mbit/s ATM-SDH/SONET framer IC**

*Koziotis, M.; Birbas, A.; Theoharis, S.*

Electronics Letters , Volume: 35 Issue: 21 , 14 Oct. 1999

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✓ **9 Design and simulation of three ATM ASICs**

*Chan Kim; Jong Arm Jun; Sang Ho Lee; Jae Geun Kim*

ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on , 1999

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**10 The chip set HDSL modem: design and performance evaluation**

*Krstic, V.R.; Kamsek, U.A.; Kuzmanovic, A.Z.*

Telecommunications in Modern Satellite, Cable and Broadcasting Services, 1999. 4th International Conference on , Volume: 1 , 1999

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**11 A 10 Gb/s SiGe bipolar framer/demultiplexer for SDH systems**

*Shioiri, S.; Soda, M.; Morikawa, T.; Hashimoto, T.; Sato, F.; Emura, K.*

Solid-State Circuits Conference, 1998. Digest of Technical Papers. 1998 IEEE International , 1998

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**12 Design and implementation of a high-speed ATM host interface controller**

*Chan Kim; Jong-Arm Jun; Yeong-Ho Park; Kyu-Ho Lee; Hyup-Jong Kim*

Information Networking, 1998. (ICOIN-12) Proceedings., Twelfth International Conference on , 1998

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**13 SDH 10 Gb/s regenerator framer in 0.6  $\mu$ m CMOS**

*Edman, A.; Rudberg, B.*

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 43rd ISSCC., 1997 IEEE International , 1997

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**14 ATM AAL-2 chip design for video applications over ATM network**

*Yuan-Sun Chu; Ching-Lung Chang; Lian-Jyi Lin; Kim-Joan Chen*  
Communication Technology Proceedings, 1996. ICCT'96., 1996  
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**15 10 Gb/s framer/demultiplexer IC for SONET STS-192 applications**

*Bagheri, M.; Kong, D.T.; Hacker, J.; Schneider, K.; Chow, J.*  
Custom Integrated Circuits Conference, 1995., Proceedings of the  
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... 2. Testbench of ASAH-NIC Since the SDH **framer** was a schematic design, it was omitted in the VHDL **simulation** but a pseudo VHDL model was substituted for ...

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